

WEST Search History

DATE: Wednesday, April 20, 2005

Hide?	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
		<i>DB=EPAB; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L17	KR-98045853-A.did.	0
		<i>DB=PGPB; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L16	US-20020127762-A1.did.	1
<input type="checkbox"/>	L15	US-20020127762-A1.did.	1
		<i>DB=USPT; PLUR=YES; OP=ADJ</i>	
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		<i>DB=PGPB; PLUR=YES; OP=ADJ</i>	
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<input type="checkbox"/>	L11	US-20030084926-A1.did.	1
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L10	l7 and (driving unit)	5
<input type="checkbox"/>	L9	L7 and object	153
<input type="checkbox"/>	L8	L7 and wafer	1
<input type="checkbox"/>	L7	(first scanning unit) and (second scanning unit)	204
<input type="checkbox"/>	L6	(first scanning unit) and (second scanning unit)	0
<input type="checkbox"/>	L5	L2 and (second scanning)	1
<input type="checkbox"/>	L4	L3 and (second scanning)	1
<input type="checkbox"/>	L3	L2 and semiconductor	56
<input type="checkbox"/>	L2	collecting impurities	329
<input type="checkbox"/>	L1	collecting impurities	0

END OF SEARCH HISTORY

Hit List

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Generate OACS				

Search Results - Record(s) 1 through 5 of 5 returned.

☐ 1. Document ID: US 20040163670 A1

Using default format because multiple data bases are involved.

L10: Entry 1 of 5

File: PGPB

Aug 26, 2004

PGPUB-DOCUMENT-NUMBER: 20040163670

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040163670 A1

TITLE: Apparatus and method for collecting impurities on a semiconductor wafer

PUBLICATION-DATE: August 26, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ko, Yong-Kyun	Osan-city		KR	
Son, Byung-Woo	Yongin-city		KR	
Jeong, Jong-Cheol	Gyeonggi-do		KR	

US-CL-CURRENT: [134/2](#); [134/148](#), [134/153](#), [134/25.4](#), [134/34](#), [134/902](#)

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw Ds
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☐ 2. Document ID: US 20020121734 A1

L10: Entry 2 of 5

File: PGPB

Sep 5, 2002

PGPUB-DOCUMENT-NUMBER: 20020121734

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020121734 A1

TITLE: Sheet-like object sorter, and discharged paper processing device for image-forming apparatus employing the same

PUBLICATION-DATE: September 5, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ogita, Toshiki	Shiki-gun		JP	
Hayashi, Shigeki	Ikoma-gun		JP	
Okada, Naofumi	Tenri-shi		JP	
Nagata, Jinichi	Osaka		JP	

Tokishige, Kiyoshi	Souraku-gun	JP
Nanba, Toyoaki	Osaka	JP

US-CL-CURRENT: 270/58.08

ABSTRACT:

A plurality of bin units 91 forming a bin unit assembly are provided with respective gates 97 and, hence, it is possible to sufficiently narrow the clearance between the gates 97 and the bodies of the bin unit 91. When the gate 97 provided at each bin unit 91 is expanded to lie toward a conveying path 93, the front edge of the gate 97 becomes put into a dent portion 92a of a sidewall 92 of an image-forming apparatus 1. For this reason, any clearance or protrusion causing jamming of recording paper is not formed in a branch path extending from the conveying path 92 to a discharged paper tray 99 through the gate 97 and, hence, it is possible to guide recording paper from the conveying path into each bin unit 91 by means of the gate 97 smoothly.

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWNC	Drawings
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☐ 3. Document ID: US 6708972 B2

L10: Entry 3 of 5

File: USPT

Mar 23, 2004

US-PAT-NO: 6708972

DOCUMENT-IDENTIFIER: US 6708972 B2

TITLE: Sheet-like object sorter, and discharged paper processing device for image-forming apparatus employing the same

DATE-ISSUED: March 23, 2004

INVENTOR-INFORMATION: .

NAME	CITY	STATE	ZIP CODE	COUNTRY
Ogita; Toshiki	Nara			JP
Hayashi; Shigeki	Nara			JP
Okada; Naofumi	Tenri			JP
Nagata; Jinichi	Osaka			JP
Tokishige; Kiyoshi	Kyoto			JP
Nanba; Toyoaki	Osaka			JP

US-CL-CURRENT: 271/305; 271/297

ABSTRACT:

A plurality of bin units are provided with respective gates and, hence, it is possible to sufficiently narrow the clearance between the gates and the bodies of the bin unit. When the gate provided at each bin unit is expanded to lie toward a conveying path, the front edge of the gate becomes put into a dent portion of a sidewall of an image-forming apparatus. For this reason, any clearance or protrusion causing jamming of recording paper is not formed in a branch path extending from the conveying path to a discharged paper tray through the gate and,

hence, it is possible to guide recording paper from the conveying path into each bin unit by means of the gate smoothly.

17 Claims, 5 Drawing figures
Exemplary Claim Number: 1
Number of Drawing Sheets: 5

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWIC	Draw D
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☐ 4. Document ID: US 6141119 A

L10: Entry 4 of 5

File: USPT

Oct 31, 2000

US-PAT-NO: 6141119
DOCUMENT-IDENTIFIER: US 6141119 A

TITLE: Imaging cartridge for a computer system

DATE-ISSUED: October 31, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Tseng; Wen-Tso	Taichung			TW
Tsai; Tsung-Cheng	Hsinchu			TW
Wu; Kevin	Hsinchu			TW

US-CL-CURRENT: 358/498; 358/474

ABSTRACT:

An imaging cartridge for a computer system, which is installed in a computer system to facilitate continuous document conveying process without any removable solid document cassette and additional document positioning process, comprises a cartridge housing for accommodating required imaging components; a roller-type document feeding unit, located at one side of the cartridge housing for carrying out continuous document conveying in and out operation of the imaging cartridge; a driving unit, located inside the cartridge housing for driving the document feeding unit; and a scanning unit, located inside the cartridge housing with a distance to the document feeding unit, for converting the imaging data into electrical signals.

19 Claims, 6 Drawing figures
Exemplary Claim Number: 1
Number of Drawing Sheets: 5

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWIC	Draw D
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☐ 5. Document ID: US 5311015 A

L10: Entry 5 of 5

File: USPT

May 10, 1994

US-PAT-NO: 5311015

DOCUMENT-IDENTIFIER: US 5311015 A

**** See image for Certificate of Correction ****

TITLE: Original reading device wherein a frame supports both the original and the reading scanning system

DATE-ISSUED: May 10, 1994

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Takeuchi; Yukitoshi	Yokohama			JP

US-CL-CURRENT: 250/234; 358/497

ABSTRACT:

An original reading device includes an original mount for mounting an original, a reading scanning system for reading the original mounted on the original mount by scanning the original, and a frame for supporting the original mount. The reading scanning system is mounted on the frame at a back side of the frame.

19 Claims, 11 Drawing figures

Exemplary Claim Number: 1

Number of Drawing Sheets: 6

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWIC	Draw. De
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Clear	Generate Collection	Print	Fwd Refs	Bkwd Refs	Generate OACS
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Term	Documents
DRIVING	1787019
DRIVINGS	517
UNIT	4476868
UNITS	1563778
(7 AND (DRIVING ADJ UNIT)).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	5
(L7 AND (DRIVING UNIT)).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	5

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Search Results - Record(s) 1 through 1 of 1 returned.

☐ 1. Document ID: US 20040163670 A1

Using default format because multiple data bases are involved.

L8: Entry 1 of 1

File: PGPB

Aug 26, 2004

PGPUB-DOCUMENT-NUMBER: 20040163670

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040163670 A1

TITLE: Apparatus and method for collecting impurities on a semiconductor wafer

PUBLICATION-DATE: August 26, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ko, Yong-Kyun	Osan-city		KR	
Son, Byung-Woo	Yongin-city		KR	
Jeong, Jong-Cheol	Gyeonggi-do		KR	

US-CL-CURRENT: 134/2; 134/148, 134/153, 134/25.4, 134/34, 134/902

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw D
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Clear	Generate Collection	Print	Fwd Refs	Bkwd Refs	Generate OACS
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Term	Documents
WAFER	377216
WAFERS	125277
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(L7 AND WAFER).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	1

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Search Results - Record(s) 51 through 56 of 56 returned.

☐ 51. Document ID: JP 08264403 A

L3: Entry 51 of 56

File: DWPI

Oct 11, 1996

DERWENT-ACC-NO: 1997-195841

DERWENT-WEEK: 199718

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TITLE: Semiconductor device mfg appts - has second chamber which is disconnected from first chamber after collecting impurities contained in gas inside gas cylinder

PRIORITY-DATA: 1995JP-0067263 (March 27, 1995)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
<u>JP 08264403 A</u>	October 11, 1996		006	H01L021/02

INT-CL (IPC): H01 L 21/02; H01 L 21/3065

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	Index	Draw D
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☐ 52. Document ID: JP 07201845 A, JP 3153065 B2, US 5503731 A, US 5904514 A

L3: Entry 52 of 56

File: DWPI

Aug 4, 1995

DERWENT-ACC-NO: 1995-305797

DERWENT-WEEK: 200121

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TITLE: Method for prodn. of anodic oxide electrodes of a semiconductor device, e.g. monolithic active matrix LCD - by preparing first electrode pair for anodic oxidation of finely patterned metal wiring lines and second electrode pair for collecting impurities, applying voltages to wiring lines, etc., preventing impurities in wiring line films

INVENTOR: HIROKI, M; KONUMA, T ; KOYAMA, ; YAMAZAKI, S ; KOYAMA, J

PRIORITY-DATA: 1993JP-0354094 (December 27, 1993)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
<u>JP 07201845 A</u>	August 4, 1995		011	H01L021/316
<u>JP 3153065 B2</u>	April 3, 2001		011	H01L021/316
<u>US 5503731 A</u>	April 2, 1996		015	C25D005/02
<u>US 5904514 A</u>	May 18, 1999		000	H01L021/84

INT-CL (IPC): C25 D 5/02; C25 D 11/04; H01 L 21/316; H01 L 21/3205; H01 L 21/336;
H01 L 21/768; H01 L 21/84; H01 L 29/786

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw D
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☐ 53. Document ID: JP 04085926 A

L3: Entry 53 of 56

File: DWPI

Mar 18, 1992

DERWENT-ACC-NO: 1992-145836

DERWENT-WEEK: 199218

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TITLE: Mfg. MOS IC semiconductor device - includes forming impurity layer and amorphous layer, and collecting impurity to amorphous layer, for high reliability
NoAbstract Dwg 1/4

PRIORITY-DATA: 1990JP-0202084 (July 30, 1990)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
<u>JP 04085926 A</u>	March 18, 1992		008	

INT-CL (IPC): H01L 21/26; H01L 29/62

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw D
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☐ 54. Document ID: EP 119103 A, CA 1206244 A, JP 59177922 A, KR 9203309 B1, US 4479455 A

L3: Entry 54 of 56

File: DWPI

Sep 19, 1984

DERWENT-ACC-NO: 1984-232657

DERWENT-WEEK: 198438

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TITLE: Semiconductor layer deposition appts. - with improved process gas channelling and dopant gas feed systems

INVENTOR: DIDIO, G M; DOEHLER, J ; HOFFMAN, K R ; LAARMAN, T D ; MCDONOUGH, T

PRIORITY-DATA: 1983US-0475289 (March 14, 1983)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
<u>EP 119103 A</u>	September 19, 1984	E	035	
<u>CA 1206244 A</u>	June 17, 1986		000	
<u>JP 59177922 A</u>	October 8, 1984		000	
<u>KR 9203309 B1</u>	April 27, 1992		000	H01L021/205
<u>US 4479455 A</u>	October 30, 1984		000	

INT-CL (IPC): C23C 11/00; C23C 13/10; H01L 21/20; H01L 21/205; H01L 31/04

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWIC	Draw. D.
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☐ 55. Document ID: EP 54657 A, DE 3048114 A, DE 3174814 G, EP 54657 B, JP 57129898 A, JP 91048159 B

L3: Entry 55 of 56

File: DWPI

Jun 30, 1982

DERWENT-ACC-NO: 1982-55177E

DERWENT-WEEK: 198227

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TITLE: High purity silicon rod made by zone melting - in oxygen atmos., so oxygen enters rod and acts as internal getter collecting impurities in semiconductor chips

INVENTOR: MUEHLBAUER, A

PRIORITY-DATA: 1980DE-3048114 (December 19, 1980)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
<u>EP 54657 A</u>	June 30, 1982	G	013	
<u>DE 3048114 A</u>	July 29, 1982		000	
<u>DE 3174814 G</u>	July 17, 1986		000	
<u>EP 54657 B</u>	June 11, 1986	G	000	
<u>JP 57129898 A</u>	August 12, 1982		000	
<u>JP 91048159 B</u>	July 23, 1991		000	

INT-CL (IPC): C30B 13/12; C30B 29/06; H01L 21/18

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWIC	Draw. D.
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☐ 56. Document ID: DD 153939 A

L3: Entry 56 of 56

File: DWPI

Feb 10, 1982

DERWENT-ACC-NO: 1982-59553E

DERWENT-WEEK: 198229

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TITLE: Getter for collecting impurities in semiconductors - esp. where silicon substrate is coated with non:orient silicon layer and tempered at high temp. so impurities enter coating

INVENTOR: KREYSCH, G; MAASS, K

PRIORITY-DATA: 1980DD-0224851 (October 31, 1980)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
<u>DD 153939 A</u>	February 10, 1982		007	

INT-CL (IPC): H01L 21/32

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWIC	Draw. D.
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Clear	Generate Collection	Print	Fwd Refs	Bkwd Refs	Generate OACS
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Term	Documents
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SEMICONDUCTORS	85372
(2 AND SEMICONDUCTOR).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	56
(L2 AND SEMICONDUCTOR).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	56

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